# BIR TIKENDRAJIT UNIVERSITY

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

## CONSOLIDATED STATEMENT OF MARKS

## BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)

Student: DEVIKA ANIL

Name: ANILKUMAR. S

Mother Name : RENJINI. R

Enrollment .No. : 202301910347137601

| | SUBJECT TITLE | CREDITS | INTERNAL | EXTERNAL | MARKS OBTAINED | MAX.PASS MARK | GRADE | RESULT | S.No. | SUBJECT CODE | SUBJECT TITLE | CREDITS | INTERNAL | EXTERNAL | MARKS OBTAINED |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | I Year/Semester | | | | | | | | | | II Year/Semester | | | | |
| 1 | ENGINEERING MATHEMATICS I | 4 | 20 | 45 | 65 | 100 | B | PASS | 1 | BTEC 2201 | ENGINEERING MATHEMATICS II | 4 | 20 | 43 | 63 |
| 2 | ENGINEERING PHYSICS | 4 | 21 | 44 | 65 | 100 | B | PASS | 2 | BTEC 2202 | ENGINEERING CHEMISTRY | 4 | 19 | 40 | 59 |
| 3 | TECHNICAL COMMUNICATION | 3 | 18 | 40 | 58 | 100 | C | PASS | 3 | BTEC 2203 | INTRODUCTION TO COMPUTER & PROGRAMMING | 3 | 18 | 42 | 60 |
| 4 | BASIC ELECTRICAL AND ELECTRONICS ENGINEERING | 4 | 19 | 39 | 58 | 100 | C | PASS | 4 | BTEC 2204 | BASIC MECHANICAL & CIVIL ENGINEERING | 4 | 22 | 44 | 66 |
| 5 | ENGINEERING MECHANICS | 3 | 20 | 41 | 61 | 100 | B | PASS | 5 | BTEC 2206 | ENGINEERING CHEMISTRY LAB | 1 | 23 | 64 | 87 |
| 6 | ENGINEERING GRAPHICS | 3 | 21 | 44 | 65 | 100 | B | PASS | 6 | BTEC 2207 | ENGINEERING WORKSHOP PRACTICES (CIVIL AND MECHANIC | 2 | 24 | 63 | 87 |
| 7 | ENGINEERING PHYSICS (LAB) | 1 | 24 | 60 | 84 | 100 | A | PASS | | | | | | | |
| 8 | ELECTRICAL & ELECTRONICS ENGINEERING(LAB) | 2 | 25 | 62 | 87 | 100 | A | PASS | | | | | | | |
| 9 | ENVIRONMENTAL STUDIES | 3 | 21 | 40 | 61 | 100 | B | PASS | | | | | | | |
| | III Year/Semester | | | | | | | | | | IV Year/Semester | | | | |
| 1 | ENGINEERING MATHEMATICS III | 4 | 20 | 39 | 59 | 100 | C | PASS | 1 | BTEC 4401 | ENGINEERING MATHEMATICS IV | 4 | 19 | 40 | 59 |
| 2 | ELECTRONIC DEVICES | 4 | 23 | 44 | 67 | 100 | B | PASS | 2 | BTEC 4402 | SIGNAL AND SYSTEM | 4 | 20 | 41 | 61 |
| 3 | DIGITAL SYSTEM DESIGN | 4 | 21 | 41 | 62 | 100 | B | PASS | 3 | BTEC 4403 | ANALOG CIRCUIT | 4 | 21 | 39 | 60 |
| 4 | NETWORK THEORY | 4 | 19 | 40 | 59 | 100 | C | PASS | 4 | BTEC 4404 | COMPUTER ARCHITECTURE AND MICROCONTROLLER | 4 | 20 | 47 | 67 |
| 5 | BUSINESS ECONOMICS | 3 | 40 | 46 | 86 | 100 | A | PASS | 5 | BTEC 4405 | ORGANIZATION BEHAVIOR | 3 | 22 | 43 | 65 |
| 6 | ELECTRONIC DEVICES LAB | 1 | 23 | 61 | 84 | 100 | A | PASS | 6 | BTEC 4406 | ANALOG CIRCUIT LABANALOG CIRCUIT LAB | 1 | 24 | 61 | 85 |
| 7 | DIGITAL SYSTEM DESIGN LAB | 1 | 24 | 60 | 84 | 100 | A | PASS | 7 | BTEC 4407 | MICROCONTROLLER LAB | 1 | 23 | 60 | 83 |

NSFER FROM APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY, STATE OF KERALA

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## BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)

Student: DEVIKA'ANIL

Name: ANILKUMAR. S

Mother Name : RENJINI. R

Enrollment .No. : 202301910347137601

| | SUBJECT TITLE | CREDITS | INTERNAL | EXTERNAL | MARKS OBTAINED | MAX PASS MARK | GRADE | RESULT | S.No. | SUBJECT CODE | SUBJECT TITLE | CREDITS | INTERNAL | EXTERNAL | MARKS OBTAINED |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | V Semester | | | | | | | | | | VI Semester | | | | |
| 1 | LINEAR INTEGRATED CIRCUITS | 4 | 20 | 40 | 60 | 100 | B | PASS | 1 | BTEC 6601 | VLSI DESIGN | 3 | 20 | 44 | 64 |
| 2 | DIGITAL SIGNAL PROCESSING | 4 | 19 | 42 | 61 | 100 | B | PASS | 2 | BTEC 6602 | COMPUTER NETWORK | 4 | 19 | 41 | 60 |
| 3 | ANALOG AND DIGITAL COMMUNICATION | 4 | 21 | 39 | 60 | 100 | B | PASS | 3 | BTEC 6603 | MOBILE COMMUNICATION AND NETWORK | 4 | 20 | 43 | 63 |
| 4 | CONTROL SYSTEMS | 4 | 20 | 37 | 57 | 100 | C | PASS | 4 | BTEC 6604 | CORE ELECTIVE I (POWER ELECTRONICS) | 3 | 21 | 49 | 70 |
| 5 | ELECTROMAGNETIC WAVES | 3 | 20 | 44 | 64 | 100 | B | PASS | 5 | BTEC 6605 | CORE ELECTIVE II (OPTIC FIBER COMMUNICATION) | 3 | 23 | 40 | 63 |
| 6 | EMBEDDED SYSTEM | 4 | 21 | 40 | 61 | 100 | B | PASS | 6 | BTEC 6606 | OPEN ELECTIVE I (CYBER SECURITY) | 3 | 22 | 39 | 61 |
| 7 | LINEAR INTEGRATED CIRCUITS LAB | 1 | 23 | 51 | 74 | 100 | B+ | PASS | 7 | BTEC 6607 | COMMUNICATION LAB | 1 | 24 | 56 | 80 |
| 8 | DIGITAL SIGNAL PROCESSING LAB | 1 | 24 | 53 | 77 | 100 | B+ | PASS | 8 | BTEC 6608 | MINI PROJECT | 2 | 24 | 53 | 77 |
| | VII Semester | | | | | | | | | | VIII Semester | | | | |
| 1 | INFORMATION THEORY AND CODING | 3 | 21 | 39 | 60 | 100 | B | PASS | 1 | BTEC 8801 | MAJOR PROJECT PHASE II (SEMINAR AND VIVA VOCE) | 12 | 25 | 59 | 84 |
| 2 | CORE ELECTIVE III (ANTENNA AND WAVE PROPAGATION) | 3 | 20 | 43 | 63 | 100 | B | PASS | | | | | | | |
| 3 | CORE ELECTIVE IV (SATELLITE COMMUNICATION) | 3 | 23 | 48 | 71 | 100 | B+ | PASS | | | | | | | |
| 4 | OPEN ELECTIVE II (RENEWABLE ENERGY SYSTEM) | 3 | 24 | 42 | 66 | 100 | B | PASS | | | | | | | |
| 5 | MAJOR PROJECT PHASE I | 2 | 24 | 55 | 79 | 100 | B+ | PASS | | | | | | | |
| 6 | INTERNSHIP | 5 | 25 | 58 | 83 | 100 | A | PASS | | | | | | | |

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| M/YEAR | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | Grand Total | Result | SGPA |
|---|---|---|---|---|---|---|---|---|---|---|---|
| l Marks | 900 | 600 | 700 | 700 | 800 | 800 | 600 | 100 | 5200 | PASS | 7 |
| Obtained | 604 | 422 | 501 | 480 | 534 | 538 | 422 | 84 | 3585 | | |

r of final exam :