

BIR TIKENDRAJIT UNIVERSITY

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

CONSOLIDATED STATEMENT OF MARKS

BACHELOR OF TECHNOLOGY (COMPUTER SCIENCE ENGINEERING)

KUL GOPAN
KUMAR VP

Mother Name : BINU S

Enrollment .No. : 202301910347057956

SUBJECT TITLE	CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX.PASS MARK	GRADE	RESULT	S.No.	SUBJECT CODE	SUBJECT TITLE	CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX.PASS MARK	GRADE	RESULT
I Year/Semester									II Year/Semester								
ENGINEERING MATHEMATICS I	4	20	40	60	100	B	PASS	1	BTCSE 2201	ENGINEERING MATHEMATICS II	4	20	41	61	100	B	PASS
ENGINEERING PHYSICS	4	21	41	62	100	B	PASS	2	BTCSE 2202	ENGINEERING CHEMISTRY	4	19	45	64	100	B	PASS
ENGLISH COMMUNICATION	3	19	44	63	100	B	PASS	3	BTCSE 2203	INTRODUCTION TO COMPUTER & PROGRAMMING	3	21	43	64	100	B	PASS
INDUSTRIAL AND ELECTRONICS ENGINEERING	4	19	45	64	100	B	PASS	4	BTCSE 2204	BASIC MECHANICAL & CIVIL ENGINEERING	4	20	44	64	100	B	PASS
ENGINEERING MECHANICS	3	20	40	60	100	B	PASS	5	BTCSE 2205	ENVIRONMENTAL STUDIES	3	20	47	67	100	B	PASS
ENGINEERING GRAPHICS	3	21	44	65	100	B	PASS	6	BTCSE 2206	ENGINEERING CHEMISTRY LAB	1	23	61	84	100	A	PASS
ENGINEERING PHYSICS (LAB)	1	23	60	83	100	A	PASS	7	BTCSE 2207	ENGINEERING WORKSHOP PRACTICES(CIVIL AND MECHANICAL)	2	23	60	83	100	A	PASS
INDUSTRIAL & ELECTRONICS ENGINEERING(LAB)	2	23	60	83	100	A	PASS										
III Year/Semester									IV Year/Semester								
ENGINEERING MATHEMATICS III(DISCRETE MATHEMATICS)	4	20	40	60	100	B	PASS	1	BTCSE 4401	COMPUTER ORGANIZATION AND ARCHITECTURE	4	20	40	60	100	B	PASS
STRUCTURE AND ALGORITHM	4	20	44	64	100	B	PASS	2	BTCSE 4402	DESIGN AND ANALYSIS OF ALGORITHM	4	19	44	63	100	B	PASS
INDUSTRIAL ELECTRONICS	4	19	41	60	100	B	PASS	3	BTCSE 4403	DATA BASE MANAGEMENT SYSTEM	4	20	41	61	100	B	PASS
ORIENTED PROGRAMMING	4	20	45	65	100	B	PASS	4	BTCSE 4404	OPERATING SYSTEM	4	20	40	60	100	B	PASS
ECONOMICS	3	21	40	61	100	B	PASS	5	BTCSE 4405	ORGANIZATION BEHAVIOR	3	22	44	66	100	B	PASS
STRUCTURE AND ALGORITHM LAB	2	23	64	87	100	A	PASS	6	BTCSE 4406	DESIGN AND ANALYSIS OF ALGORITHM LAB	2	23	63	86	100	A	PASS
INDUSTRIAL ELECTRONICS LAB	1	23	61	84	100	A	PASS	7	BTCSE 4407	OPERATING SYSTEM LAB	1	23	60	83	100	A	PASS
ORIENTED PROGRAMMING LAB	1	23	63	86	100	A	PASS										

M APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY, KERALA



(Signature)

Registrar / Controller of Examination

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V Semester									VI Semester								
COMPUTATION	4	20	40	60	100	B	PASS	1	BTCSE 6601	ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING	4	20	45	65	100	B	PASS
SOFTWARE	4	19	47	66	100	B	PASS	2	BTCSE 6602	COMPUTER GRAPHICS	4	21	44	65	100	B	PASS
ESSOR	4	21	41	62	100	B	PASS	3	BTCSE 6603	COMPILER DESIGN	4	20	40	60	100	B	PASS
R NETWORK	4	20	42	62	100	B	PASS	4	BTCSE 6604	CORE ELECTIVE I (GRAPH THEORY)	3	22	48	70	100	B+	PASS
ENGINEERING	3	20	45	65	100	B	PASS	5	BTCSE 6605	CORE ELECTIVE II (DISTRIBUTED COMPUTING SYSTEM)	3	22	44	66	100	B	PASS
MANAGEMENT SYSTEM LAB	1	23	61	84	100	A	PASS	6	BTCSE 6606	OPEN ELECTIVE I (INTERNET OF THINGS)	3	22	41	63	100	B	PASS
R NETWORK LAB	1	23	63	86	100	A	PASS	7	BTCSE 6607	MINI PROJEECT	2	24	61	85	100	A	PASS
VII Semester									VIII Semester								
URITY	4	20	40	60	100	B	PASS	1	BTCSE 8801	MAJOR PROJECT PHASE II (SEMINAR AND VIVA VOCE)	12	24	60	84	100	A	PASS
TIME III (WEB TECHNOLOGY)	3	22	44	66	100	B	PASS										
TIME IV (BIG DATA ANALYTICS)	3	22	42	64	100	B	PASS										
TIME II (CLOUD COMPUTING)	3	22	45	67	100	B	PASS										
JECT PHASE I	2	24	63	87	100	A	PASS										
P	5	24	61	85	100	A	PASS										

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1	2	3	4	5	6	7	8	Grand Total	Result	SGPA/CGPA
800	700	800	700	700	700	600	100	5100	PASS	7.32
540	487	567	479	485	474	429	84	3545		



Abraham

Registrar / Controller of Examination