



ARUNACHAL PRADESH

THE GLOBAL UNIVERSITY

Established by Act No. 9/2017 of Govt. of Arunachal Pradesh u/s 2(f) of UGC Act 1956

THE GLOBAL UNIVERSITY, OPPOSITE TCL FESTIVAL GROUND, SENKI PARK, ITANAGAR, ARUNACHAL PRADESH, 791113

CONSOLIDATED STATEMENT OF MARKS

BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)

Student: AISWARYA R PILLAI
 Name: RADHAKRISHNA PILLAI R

Mother Name : RAJASREE V
 Enrollment .No. : 230325100115

SUBJECT TITLE	CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX PASS MARK	GRADE	RESULT	S.No.	SUBJECT CODE	SUBJECT TITLE	CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX PASS
I Year/Semester								II Year/Semester							
ENGINEERING MATHEMATICS I	4	20	46	66	100	B	PASS	1	BTEC 2201	ENGINEERING MATHEMATICS II	4	20	40	60	100
ENGINEERING PHYSICS	4	21	44	65	100	B	PASS	2	BTEC 2202	ENGINEERING CHEMISTRY	4	19	48	67	100
TECHNICAL COMMUNICATION	3	19	40	59	100	C	PASS	3	BTEC 2203	INTRODUCTION TO COMPUTER & PROGRAMMING	3	20	44	64	100
BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	4	22	49	71	100	B+	PASS	4	BTEC 2204	BASIC MECHANICAL & CIVIL ENGINEERING	4	22	41	63	100
ENGINEERING MECHANICS	3	20	41	61	100	B	PASS	5	BTEC 2205	ENVIRONMENTAL STUDIES	3	20	47	67	100
ENGINEERING GRAPHICS	3	20	45	65	100	B	PASS	6	BTEC 2206	ENGINEERING CHEMISTRY LAB	1	24	63	87	100
ENGINEERING PHYSICS (LAB)	1	24	59	83	100	A	PASS	7	BTEC 2207	ENGINEERING WORKSHOP PRACTICES (CIVIL AND MECHANIC)	2	24	60	84	100
ELECTRICAL & ELECTRONICS ENGINEERING(LAB)	2	24	60	84	100	A	PASS								
III Year/Semester								IV Year/Semester							
ENGINEERING MATHEMATICS III	4	20	43	63	100	B	PASS	1	BTEC 4401	ENGINEERING MATHEMATICS IV	4	20	44	64	100
ELECTRONIC DEVICES	4	19	40	59	100	C	PASS	2	BTEC 4402	SIGNAL AND SYSTEM	4	19	40	59	100
DIGITAL SYSTEM DESIGN	4	22	48	70	100	B+	PASS	3	BTEC 4403	ANALOG CIRCUIT	4	20	49	69	100
NETWORK THEORY	4	20	45	65	100	B	PASS	4	BTEC 4404	COMPUTER ARCHITECTURE AND MICROCONTROLLER	4	20	41	61	100
BUSINESS ECONOMICS	3	21	40	61	100	B	PASS	5	BTEC 4405	ORGANIZATION BEHAVIOR	3	22	44	66	100
ELECTRONIC DEVICES LAB	1	24	63	87	100	A	PASS	6	BTEC 4406	ANALOG CIRCUIT LAB	1	24	63	87	100
DIGITAL SYSTEM DESIGN LAB	1	24	59	83	100	A	PASS	7	BTEC 4407	MICROCONTROLLER LAB	1	24	60	84	100

TRANSFER FROM APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY, KERALA



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THE GLOBAL UNIVERSITY ADMINISTRATIVE OFFICE: MODEL VILLAGE NAHARLAGUN, ITANAGAR, ARUNACHAL PRADESH, 791110

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V Semester								VI Semester							
LINEAR INTEGRATED CIRCUITS	4	20	44	64	100	B	PASS	1	BTEC 6601	VLSI DESIGN	3	20	40	60	100
DIGITAL SIGNAL PROCESSING	4	20	44	64	100	B	PASS	2	BTEC 6602	COMPUTER NETWORK	4	19	44	63	100
ANALOG AND DIGITAL COMMUNICATION	4	22	49	71	100	B+	PASS	3	BTEC 6603	MOBILE COMMUNICATION AND NETWORK	4	22	49	71	100
CONTROL SYSTEMS	4	19	45	64	100	B	PASS	4	BTEC 6604	CORE ELECTIVE I (POWER ELECTRONICS)	3	21	40	61	100
ELECTROMAGNETIC WAVES	3	21	41	62	100	B	PASS	5	BTEC 6605	CORE ELECTIVE II (OPTIC FIBER COMMUNICATION)	3	21	41	62	100
EMBEDDED SYSTEM	4	20	40	60	100	B	PASS	6	BTEC 6606	OPEN ELECTIVE I (CYBER SECURITY)	3	21	48	69	100
LINEAR INTEGRATED CIRCUITS LAB	1	24	59	83	100	A	PASS	7	BTEC 6607	COMMUNICATION LAB	1	24	58	82	100
DIGITAL SIGNAL PROCESSING LAB	1	24	60	84	100	A	PASS	8	BTEC 6608	MINI PROJECT	2	24	60	84	100
VII Semester								VIII Semester							
INFORMATION THEORY AND CODING	3	20	40	60	100	B	PASS	1	BTEC 8801	MAJOR PROJECT PHASE II (SEMINAR AND VIVA VOCE)	12	25	59	84	100
CORE ELECTIVE III (ANTENNA AND WAVE PROPAGATION)	3	21	45	66	100	B	PASS								
CORE ELECTIVE IV (SATELLITE COMMUNICATION)	3	21	49	70	100	B+	PASS								
OPEN ELECTIVE II (RENEWABLE ENERGY SYSTEM)	3	21	41	62	100	B	PASS								
MAJOR PROJECT PHASE I	2	24	62	86	100	A	PASS								
INTERNSHIP	5	24	60	84	100	A	PASS								

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YEAR	1	2	3	4	5	6	7	8	Grand Total	Result	SGPA/C
Marks	900	700	700	700	800	800	600	100	5200	PASS	7.37
Marks Obtained	554	482	488	490	552	552	428	84	3840		