

# BIR TIKENDRAJIT UNIVERSITY

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

## CONSOLIDATED STATEMENT OF MARKS

### BACHELOR OF TECHNOLOGY (ELECTRONICS & COMMUNICATION ENGINEERING)

Name of Student: SHREYAS SREEMON PILLAI  
 Father's Name: SREEMON BALAKRISHNA PILLAI

Mother Name : SREEDEVI SREEMON  
 Enrollment .No. : 202301910347167697

SUBJECT CODE	SUBJECT TITLE	CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX.PASS MARK	GRADE	RESULT	S.No.	SUBJECT CODE	SUBJECT TITLE	CREDITS	INTERNAL	EXTERNAL
I Year/Semester									II Year/Semester					
BTEC 1101	ENGINEERING MATHEMATICS I	4	19	40	59	100	C	PASS	1	BTEC 2201	ENGINEERING MATHEMATICS II	4	20	40
BTEC 1102	ENGINEERING PHYSICS	4	20	41	61	100	B	PASS	2	BTEC 2202	ENGINEERING CHEMISTRY	4	19	39
BTEC 1103	TECHNICAL COMMUNICATION	3	17	49	66	100	B	PASS	3	BTEC 2203	INTRODUCTION TO COMPUTER & PROGRAMMING	3	18	44
BTEC 1104	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING	4	18	44	62	100	B	PASS	4	BTEC 2204	BASIC MECHANICAL & CIVIL ENGINEERING	4	20	40
BTEC 1105	ENGINEERING MECHANICS	3	21	39	60	100	B	PASS	5	BTEC 2205	ENVIRONMENTAL STUDIES	3	22	46
BTEC 1106	ENGINEERING GRAPHICS	3	22	47	69	100	B	PASS	6	BTEC 2206	ENGINEERING CHEMISTRY LAB	1	24	60
BTEC 1106	ENGINEERING PHYSICS (LAB)	1	24	62	86	100	A	PASS	7	BTEC 2207	ENGINEERING WORKSHOP PRACTICES (CIVIL AND MECHANIC)	2	24	64
BTEC 1107	ELECTRICAL & ELECTRONICS ENGINEERING(LAB)	2	24	59	83	100	A	PASS						
III Year/Semester									IV Year/Semester					
BTEC 3301	ENGINEERING MATHEMATICS III	4	20	45	65	100	B	PASS	1	BTEC 4401	ENGINEERING MATHEMATICS IV	4	19	44
BTEC 3302	ELECTRONIC DEVICES	4	20	39	59	100	C	PASS	2	BTEC 4402	SIGNAL AND SYSTEM	4	20	39
BTEC 3303	DIGITAL SYSTEM DESIGN	4	21	44	65	100	B	PASS	3	BTEC 4403	ANALOG CIRCUIT	4	22	41
BTEC 3304	NETWORK THEORY	4	20	40	60	100	B	PASS	4	BTEC 4404	COMPUTER ARCHITECTURE AND MICROCONTROLLER	4	18	40
BTEC 3305	BUSINESS ECONOMICS	3	19	49	68	100	B	PASS	5	BTEC 4405	ORGANIZATION BEHAVIOR	3	20	45
BTEC 3306	ELECTRONIC DEVICES LAB	1	23	59	82	100	A	PASS	6	BTEC 4406	ANALOG CIRCUIT LAB	1	23	61
BTEC 3307	DIGITAL SYSTEM DESIGN LAB	1	24	60	84	100	A	PASS	7	BTEC 4407	MICROCONTROLLER LAB	1	24	63

ADMITTED FROM JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD , STATE OF TELANGANA



*(Signature)*



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Name of Student: SHREYAS SREEMON PILLAI

Mother Name : SREEDEVI SREEMON

Roll No. : SREEMON BALAKRISHNA PILLAI

Enrollment .No. : 202301910347167697

S.No.	SUBJECT CODE	SUBJECT TITLE	V Semester							VI Semester							
			CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX. PASS MARK	GRADE	RESULT	CREDITS	INTERNAL	EXTERNAL	MARKS OBTAINED	MAX. PASS MARK	GRADE	RESULT	
01	BTEC 6601	VLSI DESIGN	4	19	44	63	100	B	PASS	3	20	44	64	100	B	PASS	
02	BTEC 6602	COMPUTER NETWORK	4	20	40	60	100	B	PASS	4	19	41	60	100	B	PASS	
03	BTEC 6603	MOBILE COMMUNICATION AND NETWORK	4	22	49	71	100	B+	PASS	4	20	40	60	100	B	PASS	
04	BTEC 6604	CORE ELECTIVE I (POWER ELECTRONICS)	4	23	41	64	100	B	PASS	3	20	43	63	100	B	PASS	
05	BTEC 6605	CORE ELECTIVE II (OPTIC FIBER COMMUNICATION)	3	21	47	68	100	B	PASS	3	19	47	66	100	B	PASS	
06	BTEC 6606	OPEN ELECTIVE I (CYBER SECURITY)	4	20	40	60	100	B	PASS	3	21	40	61	100	B	PASS	
07	BTEC 6607	COMMUNICATION LAB	1	24	63	87	100	A	PASS	1	24	61	85	100	A	PASS	
08	BTEC 6608	MINI PROJECT	1	24	60	84	100	A	PASS	2	23	58	82	100	A	PASS	
VII Semester									VIII Semester								
01	BTEC 8801	MAJOR PROJECT PHASE II (SEMINAR AND VIVA VOCE)	3	20	44	64	100	B	PASS	12	24	60	84	100	B	PASS	
02		CORE ELECTIVE III (ANTENNA AND WAVE PROPAGATION)	3	19	40	59	100	C	PASS								
03		CORE ELECTIVE IV (SATELLITE COMMUNICATION)	3	21	43	64	100	B	PASS								
04		OPEN ELECTIVE II (RENEWABLE ENERGY SYSTEM)	3	20	44	64	100	B	PASS								
05		MAJOR PROJECT PHASE I	2	23	58	81	100	A	PASS								
06		INTERNSHIP	5	25	56	81	100	A	PASS								

#### TRANSFER FROM JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD, STATE OF TELANGANA

SEM/YEAR	1	2	3	4	5	6	7	8	Grand Total	Result	SGPA
Full Marks	800	700	700	700	800	800	600	100	5200	PASS	7.2
Mark Obtained	546	480	483	479	557	541	413	84	3583		

Year of final exam :