

**BIR TIKENDRAJIT UNIVERSITY**

Established Under Section 2(f) UGC Act, 1956

IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS**BACHELOR OF TECHNOLOGY (ELECTRICAL & ELECTRONICS ENGINEERING)**

Name: ABIN JOSEPH

Seat No.: 2023041038675

Father's Name: JOSEPH DEVASIA

Year/Sem: III

Enrollment .No. : 202206910347197597

Month & Year Of Exam : January 2023

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
301	NETWORKS & SYSTEMS	40	24	60	34	100	58	P
302	ENGINEERING MATHEMATICS -III	40	20	60	43	100	63	P
303	ELECTRICAL MEASUREMENT & MEASURING INSTRUMENTS	40	25	60	41	100	66	P
304	SOLID STATE DEVICES & CIRCUITS	40	27	60	39	100	66	P
305	SWITCHING THEORY & LOGIC DESIGN	40	25	60	44	100	69	P
Grand Total						500	322	64.40%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 10-Apr-2023



Controller of Examinations/Dy. Registrar



Serial No: BOM607



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BACHELOR OF TECHNOLOGY (ELECTRICAL & ELECTRONICS ENGINEERING)

Name: ABIN JOSEPH

Seat No.: 2023072555271

Father's Name: JOSEPH DEVASIA

Year/Sem: IV

Enrollment .No. : 202206910347197597

Month & Year Of Exam : July 2023

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks	
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured		
401	COMPUTER ORGANIZATIONS	40	24	60	34	100	58	P	
402	ELECTRICAL MACHINES - I	40	20	60	43	100	63	P	
403	ELECTRICAL ENGINEERING MATERIALS	40	27	60	41	100	68	P	
404	ELECTROMAGNETIC FIELD THEORY	40	22	60	48	100	70	P	
405	ELECTRICAL ENGINEERING DRAWING	40	27	60	43	100	70	P	
Grand Total							500	329	65.80%
Result									PASS

Remarks: P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 25-Jul-2023



Controller of Examinations/Dy. Registrar



Serial No:KVH7



BIR TIKENDRAJIT UNIVERSITY

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IMPHAL MANIPUR, INDIA

STATEMENTS OF MARKS

BACHELOR OF TECHNOLOGY (ELECTRICAL & ELECTRONICS ENGINEERING)

Name: ABIN JOSEPH

Seat No.: 2024030151703

Father's Name: JOSEPH DEVASIA

Year/Sem: V

Enrollment .No. : 202206910347197597

Month & Year Of Exam : January 2024

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
501	ELEMENTS OF ECONOMICS & PRINCIPLE OF MANAGEMENT SCIENCE	40	24	60	34	100	58	P
502	ELECTRICAL MACHINES - II	40	20	60	43	100	63	P
503	ELEMENTS OF POWER SYSTEM	40	27	60	41	100	68	P
504	ELECTRONICS CIRCUIT DESIGN	40	20	60	39	100	59	P
505	UTILIZATION OF ELECTRICAL ENERGY	40	27	60	43	100	70	P
Grand Total						500	318	63.60%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 01-Mar-2024



Controller of Examinations/Dy. Registrar



Serial No:EVK049



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STATEMENTS OF MARKS

BACHELOR OF TECHNOLOGY (ELECTRICAL & ELECTRONICS ENGINEERING)

Name: ABIN JOSEPH

Seat No.: 2024071568999

Father's Name: JOSEPH DEVASIA

Year/Sem: VI

Enrollment .No. : 202206910347197597

Month & Year Of Exam : June 2024

Paper Code	Paper Title	Internal Assessment		External Assessment		Total		Remarks
		Maximum Marks	Marks Secured	Maximum Marks	Marks Secured	Max Marks	Marks Secured	
601	POWER ELECTRONICS	40	25	60	34	100	59	P
602	POWER SYSTEM ANALYSIS	40	20	60	43	100	63	P
603	ELECTRICAL MACHINE DESIGN - I	40	27	60	34	100	61	P
604	ADVANCE ELECTRICAL MACHINE	40	22	60	47	100	69	P
605	SEMINAR - I	40	20	60	43	100	63	P
Grand Total						500	315	63.00%
Result								PASS

Remarks:P- Pass; F- Fail; A- Absent Gm Grace Marks

Date of issue: 15-Jul-2024



Controller of Examinations/Dy. Registrar

